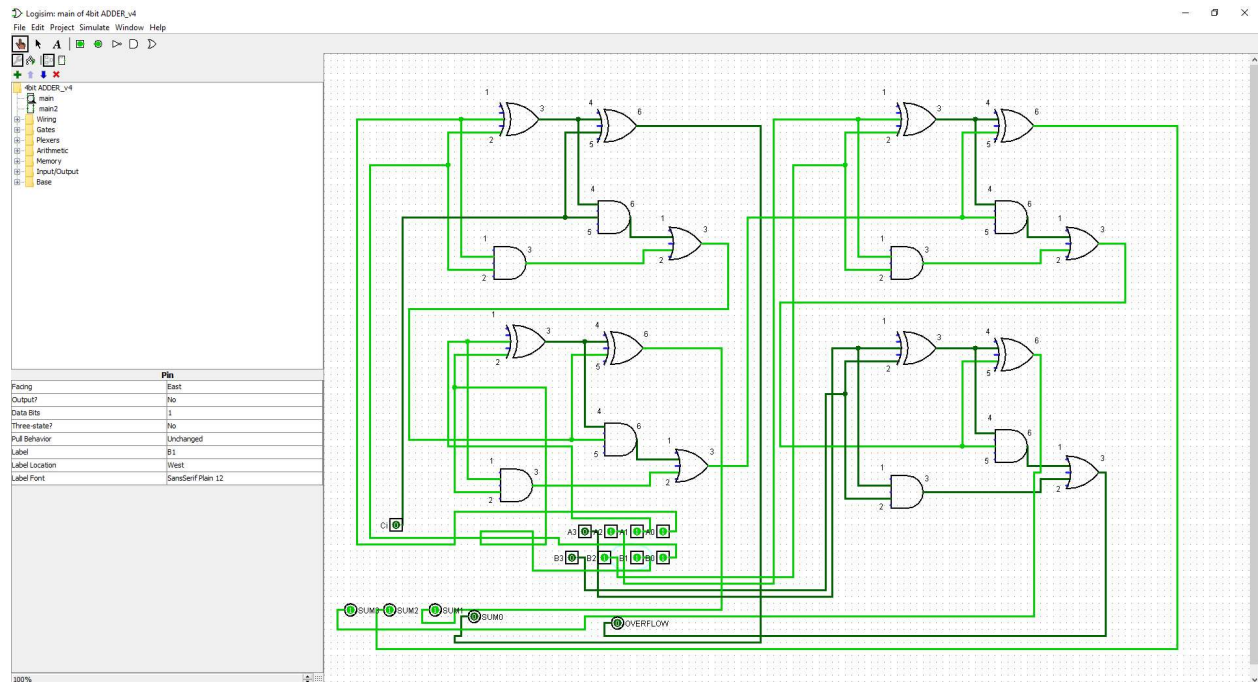


Truth Table:

[illegible]



Examples:

0001	1111
+0110	+1111
0111	11101 (1110 overflow 1)

1001	0110
+0001	+0010
1010	1000

For my 4-bit binary adder, I stacked four full-adders in order to add together two 4-bit inputs. In case of overflow, the final carry out acts as an alert bit.

